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# Memory Bytes History Technology And Digital Culture

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**page-erasable serial flash memory with byte-alterability and a 50 mhz spi bus interface features spi bus**  
**compatible serial interface 50 mhz clock rate (maximum) 2.7 v to 3.6 v single supply voltage 4 mbits of page-**  
**erasable flash memory page size: 256 bytes: - page write in 11 ms (typical) - page program in 0.8 ms (typical)**  
**- page erase in 10 ms (typical) sector erase ... memory bytes: history, technology, and digital culture ...**  
**- 706 canadian journal of communication, vol. 30 (4) the concluding section, "digital aesthetics, social texts,**  
**and art objects," consists of essays that "examine new art objects that are the result of recent digital**  
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**trading memory for disc accesses and the 5 byte rule for trading memory for cpu time jim gray franco putzolu**  
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**memory and modula-2/vrs page 2 sure pdp-11 is a 16 bit computer within four years, the memory prices**  
**dropped by a factor of 16. the unofficial history of 8051. - efont homepage - the unofficial history of**  
**8051. by jan waclawek (wek at efont) preface in 2005, the 8051 microcontroller celebrated it's 25th**  
**anniversary. i thought intel will grab on the s25fl512s, 512 mbit (64 mbyte), 3.0 v spi flash memory -**  
**s25fl512s 512 mbit (64 mbyte), 3.0 v spi flash memory cypresssemiconductorcorporation • 198 champion**  
**court • sanjose,ca 95134-1709 • 408-943-2600 the memory hierarchy - carnegie mellon university -**  
**chapter 6 the memory hierarchy to this point in our study of systems, we have relied on a simple model of a**  
**computer system as a cpu that executes instructions and a memory system that holds instructions and data**  
**for the cpu.**

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